



Product/Process Change Notice - PCN 09_0250 Rev. A

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This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

Note: Revised fields are indicated by a red field name. See Appendix B for revision history.

PCN Title: Revision of Idd Specification in ADuC7060 and ADuC7061 Data Sheet

Publication Date: 01-Feb-2010

Effectivity Date: 01-Feb-2010 (the earliest date that a customer could expect to receive changed material)

Revision Description:

Advising of an additional specification change and attaching the correct revision of the data sheet.

Description Of Change

ADuC7060/1 Idd specification change:

From: Maximum rating of 2.8mA with ADC on/DAC off, MCU clock = 1.28MHz.

To: Maximum rating of 3.1mA with ADC0 on, SPI On /DAC off/ADC1 off, MCU clock = 640 kHz and POWCON1=0x4.

Over a reduced temperature range of -40C to +85C and the same conditions, the maximum specification is 2.74mA

Other typical Idd specifications are also being added to the datasheet:

IDD (Primary ADC) - Current is dependent on Gain setting

ADC0 on, G=1, Normal Mode = 30uA (typ.)

ADC0 on, G=4, Normal Mode = 440uA (typ.)

ADC0 on, G=>128, Normal Mode = 630uA (typ.)

IDD (Auxiliary ADC) Normal mode/low power mode = 350uA/100uA (typ.)

IDD (DAC) with DAC0CON = 0x10, = 330uA (typ.)

IDD PWM = 340uA(typ.)

Rev A: The Auxiliary ADC Integral Nonlinearity specification is changed from a maximum value of +/-20ppm of FSR to a typical value of +/-15 ppm of FSR.

Reason For Change

Update of data sheet to accurately reflect device capabilities.

Impact of the change (positive or negative) on fit, form, function & reliability

With the exception of the Idd specification change above there will be no impact on the fit, form, function or reliability of the ADuC7060/1 devices.

Summary of Supporting Information

The above change will be reflected in Revision B of the ADuC7060/1 data sheet.

Supporting Documents

Attachment 1: Type: Revised Datasheet Specification

ADI_PCN_09_0250_Rev_A_ADuC7060_ADuC7061_RevB.pdf

For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative

Americas: PCN_Americas@analog.com

Europe: PCN_Europe@analog.com

Japan: PCN_Japan@analog.com

Rest of Asia: PCN_ROA@analog.com

Appendix A - Affected ADI Models**Existing Parts - Product Family / Model Number (10)**

ADUC7060 / ADUC7060BCPZ32	ADUC7060 / ADUC7060BCPZ32- RL	ADUC7060 / ADUC7060BSTZ32	ADUC7060 / ADUC7060BSTZ32-RL	ADUC7060 / EVAL- ADUC7060QSPZ	ADUC7060 / EVALADUC7060QSPZU2
ADUC7061 / ADUC7061BCPZ32	ADUC7061 / ADUC7061BCPZ32- RL	ADUC7061 / EVAL- ADUC7061MKZ	ADUC7061 / EVAL- ADUC7061MKZU2		

Appendix B - Revision History

Rev	Publish Date	Rev Description
Rev. -	28-Jan-2010	
Rev. A	01-Feb-2010	Advising of an additional specification change and attaching the correct revision of the data sheet.

Analog Devices, Inc.

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