PCN Number:	20171017002	PCN Date:	Nov.1, 2017
Title:	Datasheet for TPS548D21		
Customer Contact:	PCN Manager	Dept:	Quality Services

Change Type:

☐	Assembly Site	☐	Design	☐	Wafer Bump Site
☐	Assembly Process	☒	Data Sheet	☐	Wafer Bump Material
☐	Assembly Materials	☐	Part number change	☐	Wafer Bump Process
☐	Mechanical Specification	☐	Test Site	☐	Wafer Fab Site
☐	Packing/Shipping/Labeling	☐	Test Process	☐	Wafer Fab Materials
				☐	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.
The product datasheet(s) is being updated as summarized below.

The following change history provides further details.

TEXAS INSTRUMENTS

TPS548D21

SLUSCI8A – JULY 2016 – REVISED AUGUST 2017

Changes from Original (July 2016) to Revision A **Page**

- Changed package name to correct one 1
- Added MIN and MAX values for VDD UVLO rising threshold 5
- Added MIN and MAX for all Soft Start settings and table notes 3 and 4 in *Electrical Characteristics* 7
- Added last sentence of *Overview* to replace incomplete phrase from previous paragraph 11
- Changed V_{OUT} = 5 V to V_{OUT} = 5.5 V for Figure 12 12
- Added *Application Workaround to Support 4-ms and 8-ms SS Settings* subsection 18
- Added Figure 15 and Figure 16 18
- Deleted "programmable" between "8 ms" and "delay" 21
- Added new sentence before last sentence of *Application Information* 22
- Changed "286 µF" to "28.6 µF" 27
- Changed "150 ns" to "300 ns" in definition of $t_{OFF(min)}$, Equation 9 28
- Changed "963 µF" to "969 µF" 28

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS548D21	SLUSCI8	SLUSCI8A

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/TPS548D21/datasheet

Reason for Change:

To reflect the device pin descriptions and the interconnections in the figure accurately.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

In order to ensure either 4-ms or 8-ms soft start setting would work properly in the customer system, additional application consideration is needed. The recommended application workaround to support the 4-ms and 8-ms soft-start settings is to ensure sufficient time delay between the VDD and EN_UVLO signals. There are no changes to the actual device

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS548D21RVFR	TPS548D21RVFT		

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com