

# C3D08065E

## Silicon Carbide Schottky Diode

### Z-REC<sup>®</sup> RECTIFIER

|                               |   |       |
|-------------------------------|---|-------|
| $V_{RRM}$                     | = | 650 V |
| $I_F (T_C=135^\circ\text{C})$ | = | 12 A  |
| $Q_c$                         | = | 20 nC |

#### Features

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on  $V_F$

#### Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

#### Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives

#### Package



TO-252-2



AEC-Q101 Qualified



| Part Number | Package  | Marking  |
|-------------|----------|----------|
| C3D08065E   | TO-252-2 | C3D08065 |

#### Maximum Ratings ( $T_C = 25^\circ\text{C}$ unless otherwise specified)

| Symbol         | Parameter                                  | Value           | Unit             | Test Conditions   | Note   |
|----------------|--|-----------------|------------------|---|--------|
| $V_{RRM}$      | Repetitive Peak Reverse Voltage            | 650             | V                |   |        |
| $V_{RSM}$      | Surge Peak Reverse Voltage                 | 650             | V                |   |        |
| $V_{DC}$       | DC Blocking Voltage                        | 650             | V                |   |        |
| $I_F$          | Continuous Forward Current                 | 25.5<br>12<br>8 | A                | $T_C=25^\circ\text{C}$<br>$T_C=135^\circ\text{C}$<br>$T_C=155^\circ\text{C}$  | Fig. 3 |
| $I_{FRM}$      | Repetitive Peak Forward Surge Current      | 34<br>25        | A                | $T_C=25^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave<br>$T_C=110^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave       |        |
| $I_{FSM}$      | Non-Repetitive Peak Forward Surge Current  | 71<br>60        | A                | $T_C=25^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave<br>$T_C=110^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave       | Fig. 8 |
| $I_{F,Max}$    | Non-Repetitive Peak Forward Surge Current  | 650<br>530      | A                | $T_C=25^\circ\text{C}$ , $t_p = 10$ $\mu\text{s}$ , Pulse<br>$T_C=110^\circ\text{C}$ , $t_p = 10$ $\mu\text{s}$ , Pulse | Fig. 8 |
| $P_{tot}$      | Power Dissipation                          | 120<br>52       | W                | $T_C=25^\circ\text{C}$<br>$T_C=110^\circ\text{C}$   | Fig. 4 |
| $T_J, T_{stg}$ | Operating Junction and Storage Temperature | -55 to<br>+175  | $^\circ\text{C}$ |   |        |

## Electrical Characteristics

| Symbol | Parameter                 | Typ.            | Max.       | Unit          | Test Conditions  | Note   |
|--------|---------------------------|-----------------|------------|---------------|--|--------|
| $V_F$  | Forward Voltage           | 1.5<br>2.1      | 1.8<br>2.4 | V             | $I_F = 8\text{ A}$ $T_J = 25^\circ\text{C}$<br>$I_F = 8\text{ A}$ $T_J = 175^\circ\text{C}$  | Fig. 1 |
| $I_R$  | Reverse Current           | 10<br>20        | 50<br>200  | $\mu\text{A}$ | $V_R = 650\text{ V}$ $T_J = 25^\circ\text{C}$<br>$V_R = 650\text{ V}$ $T_J = 175^\circ\text{C}$  | Fig. 2 |
| $Q_C$  | Total Capacitive Charge   | 20              |            | nC            | $V_R = 650\text{ V}$ , $I_F = 8\text{ A}$<br>$di/dt = 500\text{ A}/\mu\text{s}$<br>$T_J = 25^\circ\text{C}$  | Fig. 5 |
| C      | Total Capacitance         | 395<br>37<br>32 |            | pF            | $V_R = 0\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$<br>$V_R = 200\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$<br>$V_R = 400\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ | Fig. 6 |
| $E_C$  | Capacitance Stored Energy | 3.0             |            | $\mu\text{J}$ | $V_R = 400\text{ V}$   | Fig. 7 |

Note: This is a majority carrier diode, so there is no reverse recovery charge.

## Thermal Characteristics

| Symbol          | Parameter                                | Typ. | Unit                      | Note   |
|-----------------|--|------|---------------------------|--------|
| $R_{\theta JC}$ | Thermal Resistance from Junction to Case | 1.25 | $^\circ\text{C}/\text{W}$ | Fig. 9 |

## Typical Performance

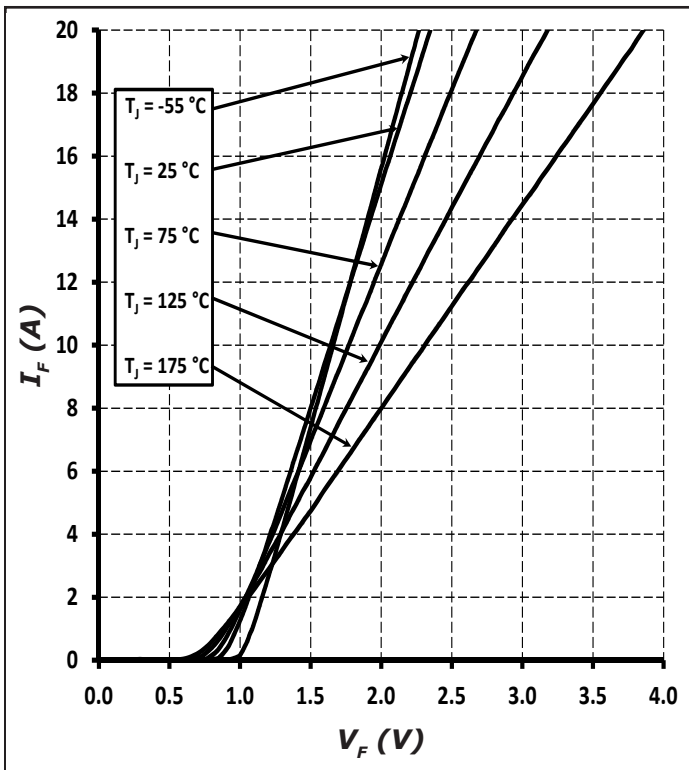


Figure 1. Forward Characteristics

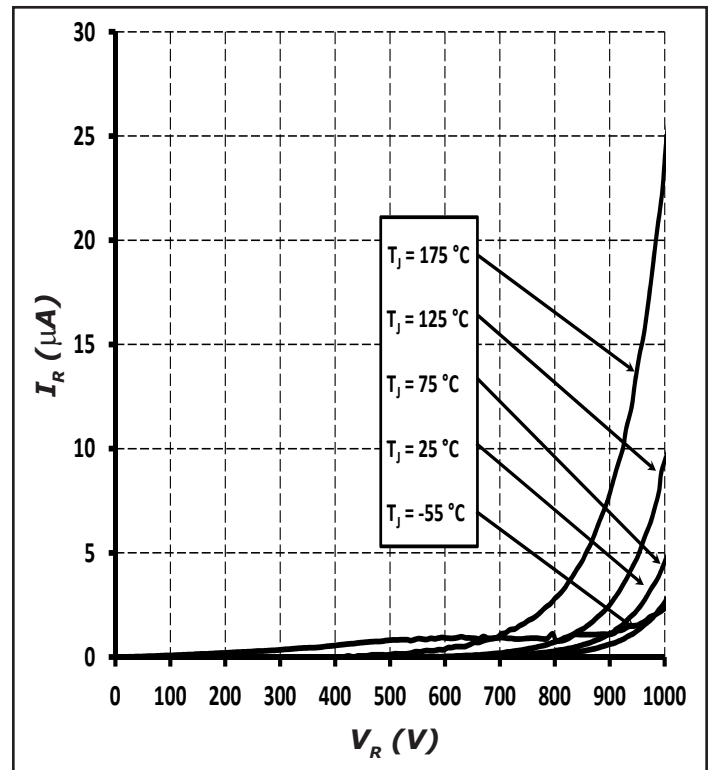


Figure 2. Reverse Characteristics

## Typical Performance

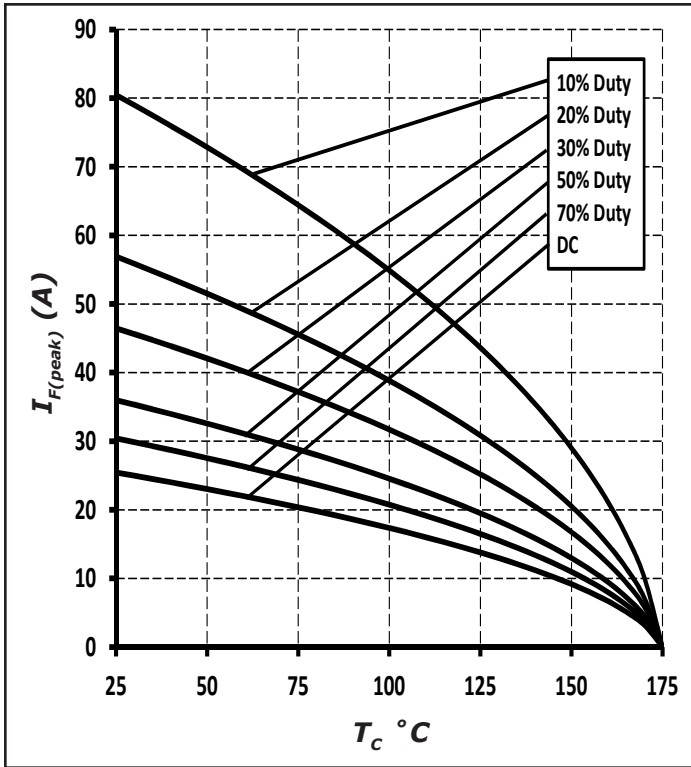


Figure 3. Current Derating

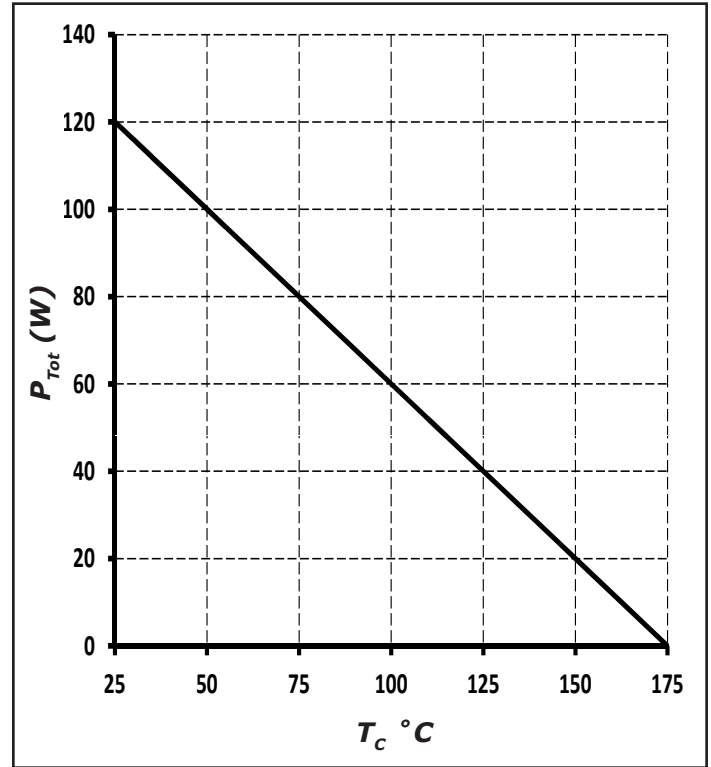


Figure 4. Power Derating

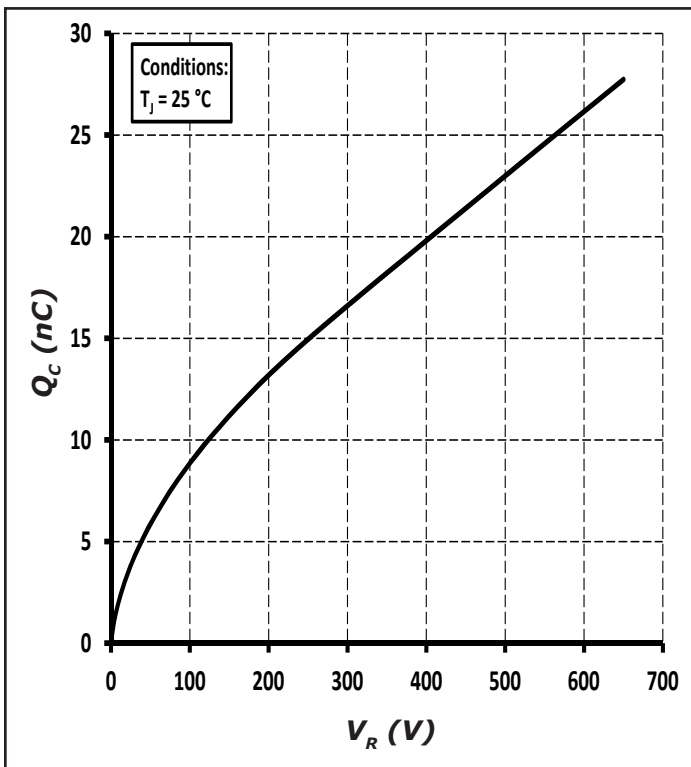


Figure 5. Total Capacitance Charge vs. Reverse Voltage

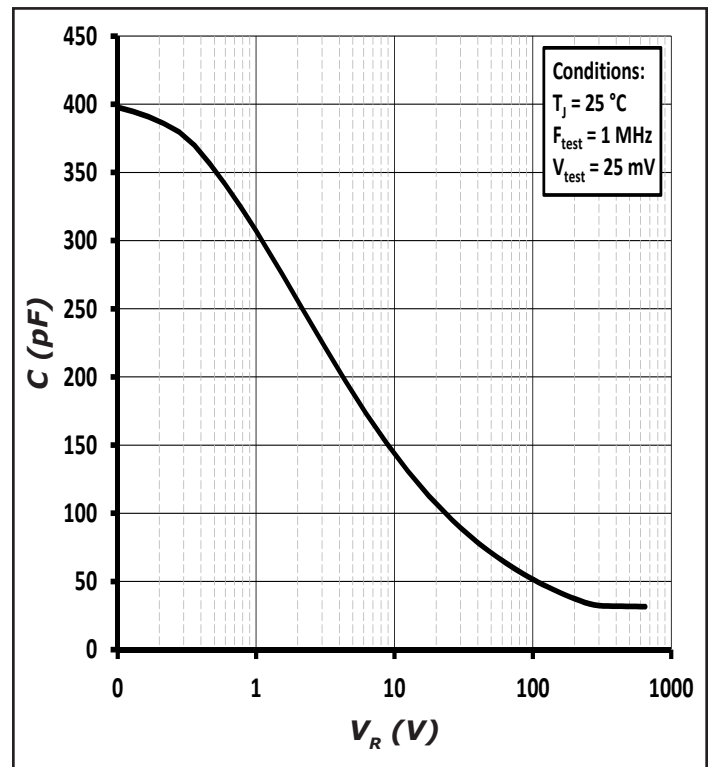


Figure 6. Capacitance vs. Reverse Voltage

## Typical Performance



Figure 7. Capacitance Stored Energy

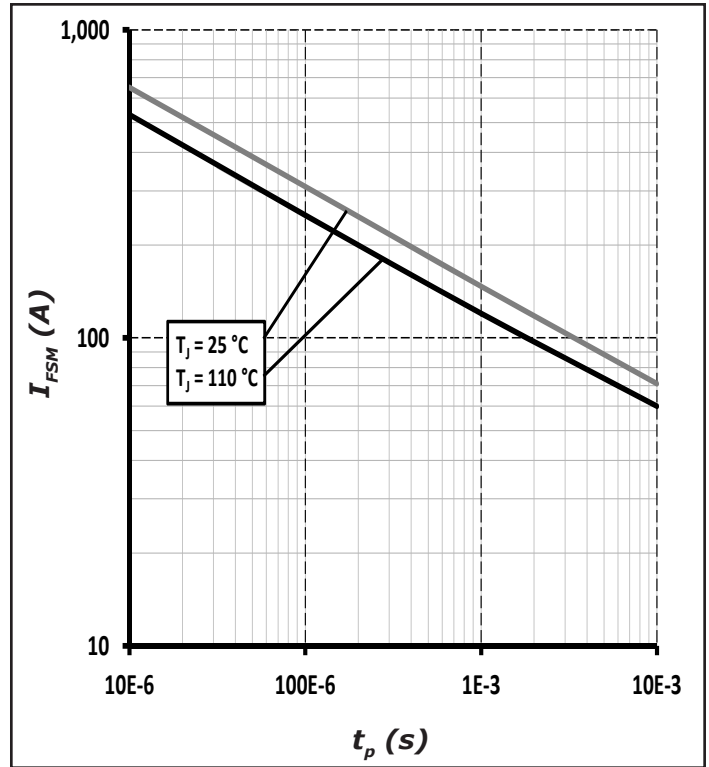


Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

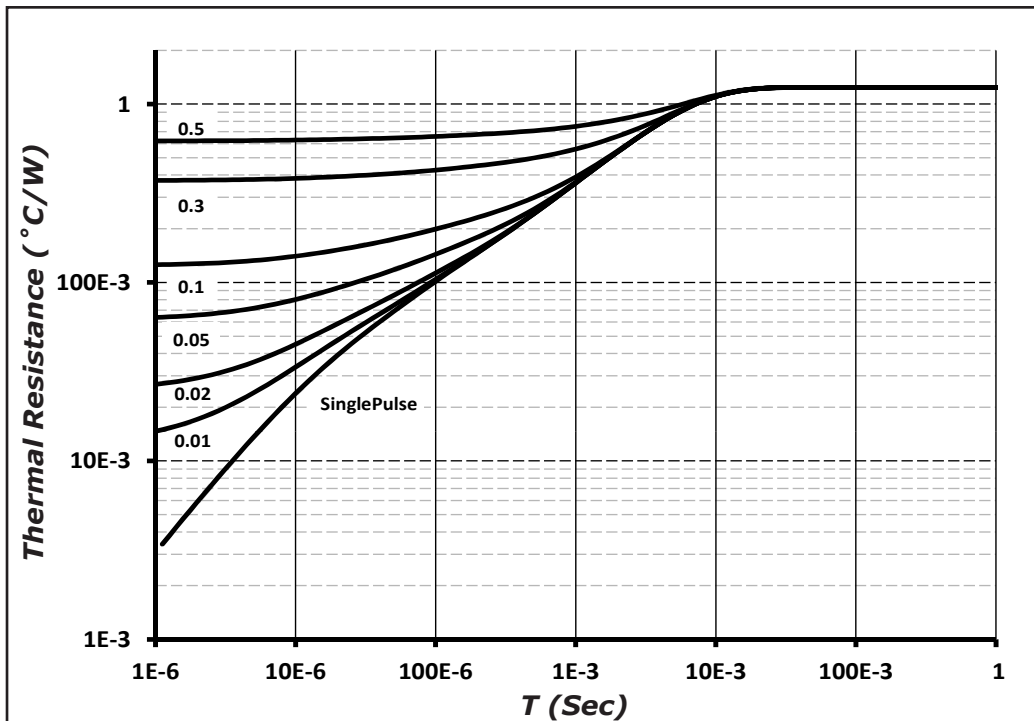
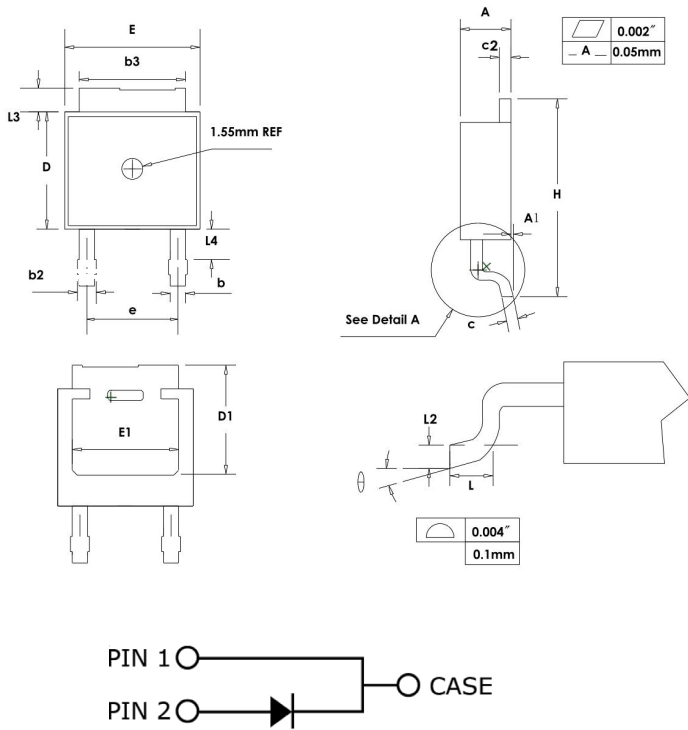


Figure 9. Transient Thermal Impedance

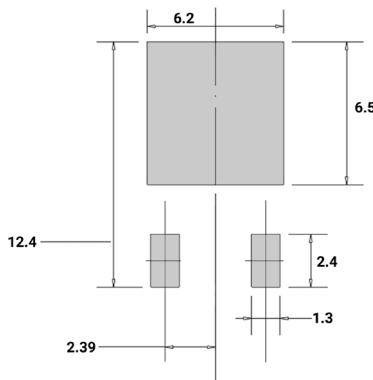
## Package Dimensions

Package TO-252-2



| SYMBOL   | MILLIMETERS |        |
|----------|-------------|--------|
|          | MIN         | MAX    |
| A        | 2.159       | 2.413  |
| A1       | 0           | 0.13   |
| b        | 0.64        | 0.89   |
| b2       | 0.653       | 1.143  |
| b3       | 5.004       | 5.6    |
| c        | 0.457       | 0.61   |
| c2       | 0.457       | 0.864  |
| D        | 5.867       | 6.248  |
| D1       | 5.21        | -      |
| E        | 6.35        | 7.341  |
| E1       | 4.32        | -      |
| e        | 4.58 BSC    |        |
| H        | 9.65        | 10.414 |
| L        | 1.106       | 1.78   |
| L2       | 0.51 BSC    |        |
| L3       | 0.889       | 1.27   |
| L4       | 0.64        | 1.01   |
| $\theta$ | 0°          | 8°     |

## Recommended Solder Pad Layout



TO-252-2

| Part Number | Package  | Marking  |
|-------------|----------|----------|
| C3D08065E   | TO-252-2 | C3D08065 |

Note: Recommended soldering profiles can be found in the applications note here: [http://www.wolfspeed.com/power\\_app\\_notes/soldering](http://www.wolfspeed.com/power_app_notes/soldering)



## Diode Model



$$V_{fT} = V_T + I_f * R_T$$

$$V_T = 0.95 + (T_j * -1.2 * 10^{-3})$$

$$R_T = 0.054 + (T_j * 5.5 * 10^{-4})$$

Note:  $T_j$  = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

## Notes

- **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Ecology section of our website at <http://www.wolfspeed.com/power/tools-and-support/product-ecology>.

- **REACH Compliance**

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

- This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

## Related Links

- Cree SiC Schottky diode portfolio: <http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes>
- Schottky diode Spice models: <http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2>
- SiC MOSFET and diode reference designs: <http://go.pardot.com/l/101562/2015-07-31/349i>