



## Product Change Notification / SYST-21WIXR141

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### Date:

22-Jun-2021

### Product Category:

32-bit Microcontrollers

### PCN Type:

Document Change

### Notification Subject:

Data Sheet - PIC32MZ Embedded Connectivity with Floating Point Unit (EF) Family Revision

### Affected CPNs:

[SYST-21WIXR141\\_Affected\\_CPN\\_06222021.pdf](#)

[SYST-21WIXR141\\_Affected\\_CPN\\_06222021.csv](#)

### Notification Text:

SYST-21WIXR141

Microchip has released a new Product Documents for the PIC32MZ Embedded Connectivity with Floating Point Unit (EF) Family of devices. If you are using one of these devices please read the document located at [PIC32MZ Embedded Connectivity with Floating Point Unit \(EF\) Family](#).

**Notification Status:** Final

**Description of Change:** This revision includes the following changes, which are referenced by their respective chapter in Table C7. In addition, minor updates to text and formatting were incorporated throughout the document. The I2C, SPI and I2S standards use the terminology "Master" and "Slave". The equivalent Microchip terminology used in this document is "Host" and "Client" respectively.

1) Device Overview 1.0

- a) Updated Table 1-6 PORTA through PORTK Pinout I/O Descriptions with new values for RD14 and RD15 for the 100-pin TFBGA package
- b) Updated Table 1-21 Power, Ground, and Voltage Reference Pinout I/O Descriptions with new VDD and VSS values for the 100-pin TFBGA package
- 2) Guidelines for Getting Started with 32-bit Microcontrollers 2.0 - Updated Non-5V tolerant input pins 2.11.1 with new values for device voltage for VDD and VSS, and updated the corresponding figure 2-10 with a new volt value for VDD
- 3) HI-Speed USB with On-The-Go (OTG) 11.0 - Updated the bitfield numbering in the description for Register 11-25 USBDPBFD: USB Double Packet Buffer Disable Register for bits 16-1
- 4) I/O Ports 12.0 - Updated note 4 of Table 12-2 Input Pin Selection
- 5) Deadman Timer (DMT) 15.0 - Updated Register 15-1 DMTCON: Deadman Timer Control Register with a new note
- 6) Power Saving Features 33.0 - Added a new note to section Peripheral Module Disable 33.3
- 7) Special Features 34.0 - Added a new note to bit 31 of Register 34-4 DEVCFG1/ADEVCFG1: Device Configuration Word 1
- 8) Electrical Characteristics 37.0 - Updated Table 37-47 EBI Timing Requirements(1) with new minimum values and a new note

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 22 June 2021

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

**Attachments:**

[PIC32MZ Embedded Connectivity with Floating Point Unit \(EF\) Family](#)

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Affected Catalog Part Numbers (CPN)

PIC32MZ0512EFE124-E/TL  
PIC32MZ0512EFF124-E/TL  
PIC32MZ0512EFK124-E/TL  
PIC32MZ1024EFE124-E/TL  
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PIC32MZ1024EFH100T-I/PTVAO  
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PIC32MZ1024EFK100T-E/GJX  
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PIC32MZ1024EFM100T-E/PT

PIC32MZ2048EFG100T-E/PT

PIC32MZ2048EFH100T-E/PT

PIC32MZ2048EFM100T-E/PT

PIC32MZ1024EFH100T-E/PTV06

PIC32MZ1024EFH100T-E/PTVAO