

<b>PCN Number:</b>	20171002000	<b>PCN Date:</b>	October 3, 2017
<b>Title:</b>	Datasheet for LMH1297		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification etc. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**LMH1297**

SNLS545B – MARCH 2017 – REVISED SEPTEMBER 2017

#### Changes from Revision A (July 2017) to Revision B

**Page**

• Changed NTps <sub>max</sub> test condition max from 6 GHz to 50 MHz and < 20 mVp-p NOM to < 10 mVp-p .....	7
• Changed t <sub>R_F_SDI</sub> minimum SD rise/fall time from 390 ps to 400 ps due to incorrect default settings.....	11
• Changed typical t <sub>R</sub> /t <sub>F</sub> of OUT0 from 35 ps to 45 ps .....	12
• Added PRBS10 pattern to clarify AJ <sub>CD_MODE</sub> and TMJ <sub>CD_MODE</sub> Test Condition .....	13
• Added SMBUS SCL min frequency per SMBus 2.0 specifications .....	14
• Added <a href="#">Figure 1</a> to clarify definition of SMBus Timing Parameters in <i>Recommended SMBus Interface Timing Specifications</i> .....	14
• Added typical characteristic curves for OUT0 VOD and de-emphasis vs. LMH1297 register settings .....	16
• Added default internal pullup and pulldown settings for 2-level strap pins in <a href="#">Figure 20</a> .....	31
• Added clarification in <a href="#">Table 13</a> regarding reclocker behavior when used with non-SDI data rates .....	32
• Changed Step 6 description in <i>Detailed Design Procedure</i> to include both pin and register settings to switch between EQ Mode and CD Mode. ....	33
• Added mechanical, thermal pad, and land pattern drawings to the <i>Package Option Addendum</i> .....	42

The datasheet number will be changing.

Device Family	Change From:	Change To:
LMH1297	SNLS545A	SNLS545B

The datasheet is considered “custom”, for that reason; the document is not available on the TI website. Please contact the TI Customer Quality Engineer (CQE) for a copy of the datasheet.

#### Reason for Change:

To more accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

#### Changes to product identification resulting from this PCN:

None.

#### Product Affected:

LMH1297RTVR	LMH1297RTVT	
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

<b>Location</b>	<b>E-Mail</b>
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
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